Docket No.: MUH-12752

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: ULLRICH MENCZIGAR

Filed : CONCURRENTLY HEREWITH

Title : SYNCHRONIZATION DEVICE FOR A SEMICONDUCTOR

MEMORY DEVICE

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,875,142 (Chevallier), dated February 23, 1999;

U.S. Patent Application Publication No. 2002/0093855 A1 (Heyne et al.), dated July 18, 2002.

LAURENCE A. GREENBERG REG. NO. 29,308

Respectfully submitted,

For Applicant

Date: September 10, 2003

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/nt/kf

U.S. DEPARTM	ENT OF	(SUBSTITUTE)	Appl. No.:					
	MATI	ON DISCLOSUF	Applicant: ULLRICH MENCZIGAR					
STATEMENT BY APPLICANT (37 CFR 1.98(b))				Filing Date: September 10, 2003 Group Art Unit:				
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILII DA	
	Α	5,875,142	2/23/99	Chevallier				
	В	2002/0093855 A1	7/18/02	Heyne et al.				
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